Smt. Poornima G N
Assistant Professor
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Work Experience :6.6 years

☐ Worked as a Lecturer in STJIT, Ranebennur**.**

Educational Qualification:

☐ M.Tech. [Digital Electronics] from GM Institute of Technology ,Davangere, Visveshwaraya Technological University, 2014, first class with distinction.

☐ B.E. [Electronics & Instrumentation] from UBDT College of Engineering & Technology from, Kuvempu University, 2010 with first class with distinction.

Subjects Taught:

Analog Electronics,Microwave and Antenna,Basic Electronics ,Operating system, HDL,Computer Communication Network,Network Security, Technological Innovation management and Entrepreneurship.

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Seminar/ Workshops Attended :

- Attended AICTE-VTU Joint Teachers Training program on Electronics Circuit Design Using LabVIEW organized by VTU- Human Resource Development Cell (HRDC),Visvesvaraya Technological University Centre for Post Graduate Studies Bengaluru Region, Muddenahalli Chikkaballapur-562101.
- Attended FDP on Google Analytics and Tools for Data Visualization organized by Department of Computer Science and Engineering in association with IQAC, ISTE and IEI-Faculty chapters BIET,Davangere.
- Teaching Learning Centre(ICT) at IIT Bombay,funded by the Pandit Madan Malaviya National Mission on Teachers and Traning (PMMMNMTT),MHRD,Govt.of India.
- Attended One week faculty development programme on "Applications of Lab view in Engineering & Research" from 28th Jan to 2nd Feb 2019 at BIET, Davangere, in association with VI Solutions, Bangalore and ISTE, IETE
- Attended a workshop on "Product Development" organized by Department of E&CE in association with Applied Cognition Sysems , Bangalore from 31st to 2nd Nov 2014.
- Attended " Faculty development Program" and one day workshop on "Plagiarism Issues" Feb 2016 organized by department of E&CE,GMIT in association with Indian Society for T Education,New Delhi.

- **Attended two day workshop on "NI LabVIEW and ARM Processor" on 27th and** 28th Feb 2015 organized by department of E&CE,GMIT in association with ISTE, advanced electronic systems,Bangalore.

Publications:

- **Presented a paper "Automated Security System" in National Conference on Emerging trends in Engineering and Communication, 2016.**

- **Presented a paper "Hardware modelling of single neuron with various activation functions using VHDL" in National Conference on Emerging trends in Engineering and Management, 2017.**